


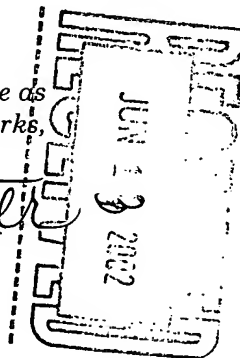


#42171  
7-5-02  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Linda L. Bolter



Applicant : Norm Hendrickson, et al.  
Application No. : 10/036,548  
Filed : December 31, 2001  
Title : REVERSE DATA DE-SKEW METHOD AND SYSTEM  
Grp./Div. : 2171  
Examiner : To Be Assigned  
Docket No. : 47653/PYI/V165

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INFORMATION DISCLOSURE STATEMENT  
37 CFR § 1.97(b)

Commissioner for Patents  
Washington, D.C. 20231

Post Office Box 7068  
Pasadena, CA 91109-7068  
May 30, 2002

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO/SB/08A/B listing the references that are known to applicant. Copies of each of the listed references are enclosed. This filing is timely because it is made during one of the periods described in 37 CFR § 1.97(b).

It is respectfully requested that the listed references be considered in the examination of this application and identified on the list of references cited on the patent issuing for this application. Applicant also requests that an initialed copy of FORM PTO/SB/08A/B be entered in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

Respectfully submitted,  
CHRISTIE, PARKER & HALE, LLP

By 

Daniel M. Cavanagh, Reg. No. 41,661  
Telephone: 626/795-9900

DMC/rmw  
Enclosures: PTO/SB/08A/B, w/references  
KMG IRV1053347.1\*-5/30/02 1:21 PM

FORM PTO/SB/08A/B (10-01)  
Substitute for PTO-1449A/B

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Attorney Docket Number 47653/PYI/V165

Application Number 10/036,548

Filing Date December 31, 2001

Applicant(s) Norm Hendrickson

Group Art Unit 2171

Examiner Name To Be Assigned

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**U.S. PATENT DOCUMENTS**

EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - kind code <sup>2</sup> . (If known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
		4,918,405	04-17-1990	Herleikson
		4,926,447	05-15-1990	Corsetto et al.
		4,970,609	11-13-1990	Cunningham et al.
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		5,592,519	01-07-1997	Honaker, Jr.
		5,610,954	03-11-1997	Miyashita et al.
		5,663,666	09-02-1997	Chu et al.
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		5,822,386	10-13-1998	Pawelski
		5,889,828	03-30-1999	Miyashita et al.
		5,905,759	05-18-1999	Ishida et al.
		5,936,445	08-10-1999	Babanezhad et al.
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		5,987,085	11-16-1999	Anderson
		6,028,903	2-22-2000	Drost et al.
		6,041,090	3-21-2000	Chen
		6,104,326	8-15-2000	Lee et al.

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Substitute for PTO-1449A/B

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Attorney Docket Number	47653/PYI/V
Application Number	10/036,548
Filing Date	December 31, 2001
Applicant(s)	Norm Hendrickson
Group Art Unit	2171
Examiner Name	To Be Assigned

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EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - kind code <sup>2</sup> . (If known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
		6,124,996	09-26-2000	Fasen et al.
		US 6,266,799 B1	07-24-2001	Lee et al.
		US 6,307,906 B1	10-23-2001	Tanji et al.
		US 6,310,498 B1	10-30-2001	Larsson
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**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIALS	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>6</sup> (✓)
		WO 98/44673	10-08-1998	Sun Microsystems, Inc.	
		WO 99/67882	12-29-1999	Xilinx, Inc.	
		EP 0 665 651 A2	08-02-1995	Hewlett-Packard Company	
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		EP 0 901 126 A1	03-10-1999	Hewlett-Packard Company	

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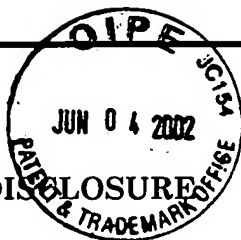
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Applicant(s)	Norm Hendrickson
Group Art Unit	2171
Examiner Name	To Be Assigned

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**OTHER DOCUMENTS**

EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		P. DARTNELL, "16 Lane SUPI - An Implementation of OC768 SFI-5 Interface", Contribution Number OIF 2000.224, Optical Internetworking Forum, November 2000 (13 pages)
		P. DARTNELL, et al., "Serdes Frammer Interface Level 5 (SFI-5): Implementation Agreement For 40Gb/s Interface For Physical Layer Devices", Contribution Number OIF 2001.145.1, Optical Internetworking Forum, April 20, 2001 (29 pgs)
		P. DARTNELL, et al., "Serdes Frammer Interface Level 5 (SFI-5): Implementation Agreement For 40Gb/s Interface For Physical Layer Devices", Contribution Number OIF 2001.145.9, Optical Internetworking Forum, November 7, 2001 (54 pgs)
		P. DARTNELL, et al., "Comment Resolution For Technical Committee Straw Ballot #17 Serdes Frammer Interface Level 5 (SFI-5), Contribution Number OIF 2001.566, Optical Internetworking Forum, October 2001
		P. DARTNELL, "SFI-5 Draft 4.0: Editors Summary and Report", Contribution Number OIF 2001.572, Optical Internetworking Forum, November 1, 2001 (8 pgs)
		M. HELLWIG, "A Proposal For A 40GBps Interface (SFI-5)", Contribution Number OIF 2000.265, Optical Internetworking Forum, November 6, 2000 (7 pgs)
		H. JOHANSEN, et al., "SFI-5, OC-768 Framers-Serdes I/F Characteristics", Contribution Number OIF 2000.227, Optical Internetworking Forum, November 7, 2000 (7 pgs)
		J. LYNCH (ed.), "OC-768 SFI-5 Ad Hoc Report", Contribution Number OIF 2000.314 Optical Internetworking Forum, November 16, 2000 (12 pgs)
		W. MOK, et al., "SFI-5: Common Electrical Interface Between Framers and Serialzer/Deserialzer Parts for STS-768/STM-192 Interfaces", Contribution Number: OIF2000.276, Optical Internetworking Forum, November 2000 (25 pgs)
		T. PALKERT, et al., "SFI-5: Common Electrical Interface Between Framers and Serialzer/Deserialzer Parts for STS-768/STM-256 Interfaces", Contribution Number OIF 2000.255, Optical Internetworking Forum, October 2000 (19 pgs)

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